UNIVERSITY OF CALIFORNIA

College of Engineering
Department of Electrical Engineering
and Computer Sciences

Professor Oldham

Fall 2000

EECS 40 — MIDTERM #2	EECS 4	0 - N	MIDT	ERM	#2
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SOLUTIONS

13 November 2000

Name: _			Stuc	lent ID:
	Last,	First		
Signatur	e:			
			TA:	☐ Ben ☐ Warren ☐ Naratip

Guidelines:

- 1. Closed book and notes except 1 page of formulas.
- 2. You may use a calculator.
- 3. Do not unstaple the exam.
- 4. Show all your work and reasoning on the exam in order to receive full or partial credit.
- 5. This exam contains 8 problems and corresponding worksheets plus the cover page.

Problem	Points Possible	Your Score
1	12	
2	12	
3	10	
4	16	
5	12	
6	12	
7	12	
8	14	
Total	100	

 $f = 10^{-15}$

 $p = 10^{-12}$

 $n = 10^{-9}$ $\mu = 10^{-6}$

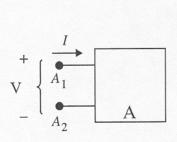
 $m = 10^{-3}$

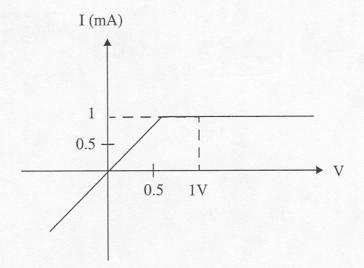
 $K = 10^3$

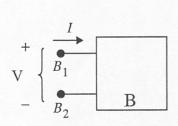
 $M = 10^6$

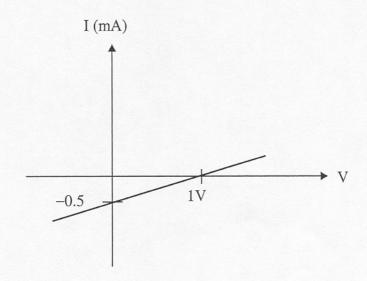
Problem 1 (12 points)

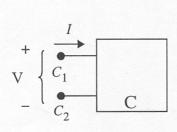
The boxes A, B, C have the following I-V characteristics:.

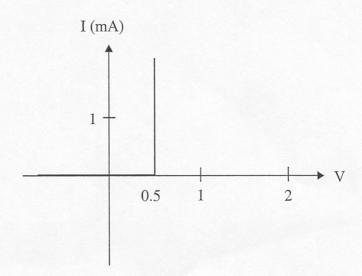








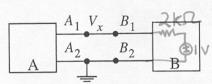




Problem 1 Answer sheet

[10 pts/Ia) Find V_x .

For B, R= 510 pe = 2KJ 500 SL For B, R= 510 pe = 2KJ V= 1V



$$V_x = \underline{0.2V}$$

So Vx should be the boint of intersection of the two curves with IB reversed

$$I_{A} = 2 \sqrt{x}$$

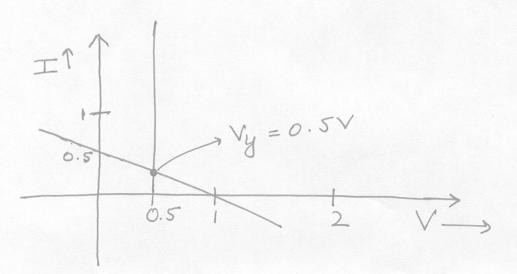
$$I_{B} = \frac{\sqrt{x-1}}{2}$$

$$T_{A}=-T_{B}$$
 $\Rightarrow 2V_{x}=\frac{1-V_{x}}{2}$ $\Rightarrow \frac{5}{2}V_{x}=\frac{1}{2}$
 $\Rightarrow V_{xx}=\frac{1}{5}=0.2V$

(10) pts/b) Find V_y .

$$\begin{array}{c|cccc} C_1 & V_y B_1 \\ \hline C_2 & B_2 \\ \hline \end{array} \quad \begin{array}{c|ccccc} B \end{array}$$

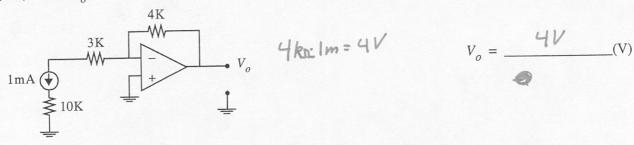
For the point of intersection of curves of Bandc with $I_c = -I_B$



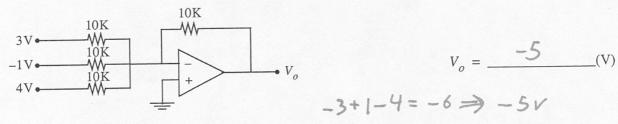
Problem 2 (12 points)

In this problem, assume that the op-amps are nearly ideal but with output voltages limited by ±5V rails.

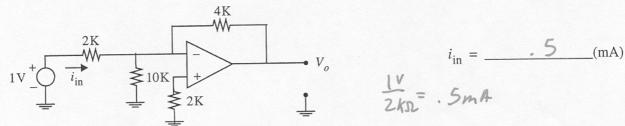
[4 pts.] a) Find V_o .



(4 pts.) b) Find V_o .



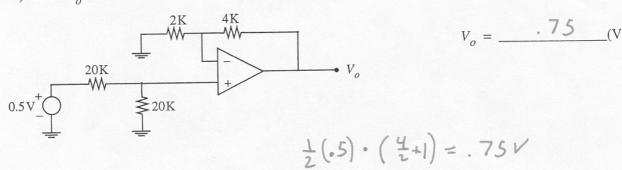
[4 pts.] c) Find $i_{\rm in}$.



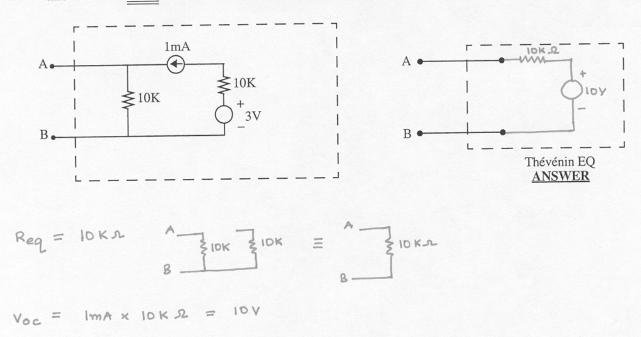
$$i_{\rm in} = \underline{\qquad . 5 \qquad (mA)}$$

$$\frac{1V}{2kn} = .5mA$$

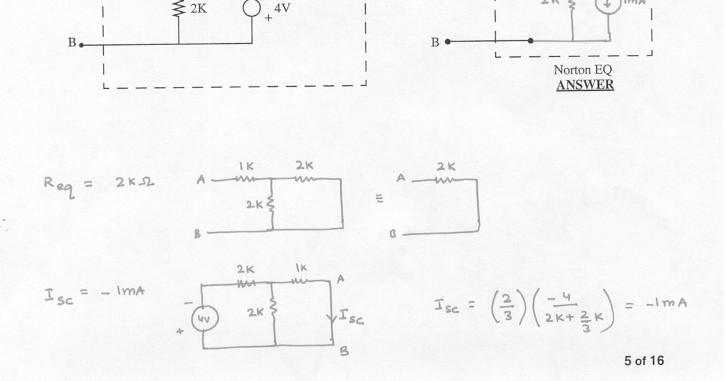
[4 pts,] d) Find V_o .



[5 pts.] a) Find the Thévénin Equivalent circuit of the stuff in the box. Draw this Thévénin circuit in the answer box. Note: No credit unless the circuit is drawn in the answer box.



[5 pts.] b) Find the Norton Equivalent circuit of the stuff in the box. Draw this Norton circuit in the answer box. Again, no credit unless the answer appears in the answer box.

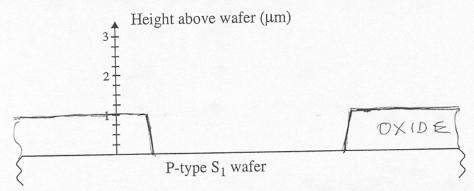


Problem 4 (16 points)

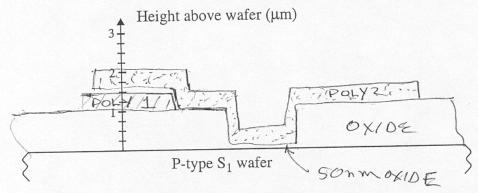
Consider the following process that refers to the figure on the opposite page.

- (1) Start with p-type wafer.
- (2) Deposit 1µm (1000nm) oxide.
- (3) Deposit 0.5µm polysilicon. ("Poly 1")
- (4) Pattern polysilicon with mask (P1). (clearfield)
- (5) Pattern oxide with mask (OX). (darkfield)
- (6) Deposit 50nm oxide.
- (7) Deposit 0.5μm polysilicon ("poly 2").
- (8) Pattern poly 2 with mask (P2). (clearfield)
- (9) Implant donors $10^{14}/\text{cm}^2$ and anneal to depth of $0.25\mu\text{m}$.
- (10) Deposit 0.5µm oxide.
- (11) Pattern oxide with mask (C). (darkfield)
- (12) Deposit 0.5 µm Al.
- (13) Pattern Al with mask (M). (darkfield) CLEAR FIELD

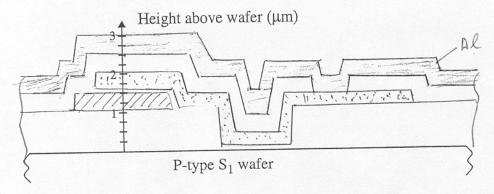
[5 pts.] a) Show cross-section A-A after completion of Step 5.



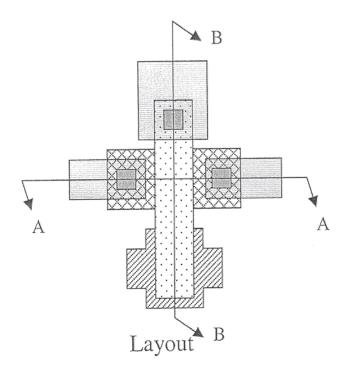
[5 pts.] b) Show cross-section B-B after completion of Step 9.

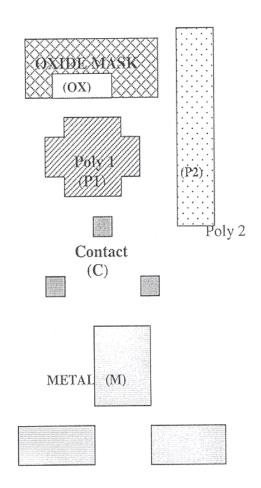


[5 pts.] c) Show cross-section B-B after completion of Step 12.



Problem 4 Figure





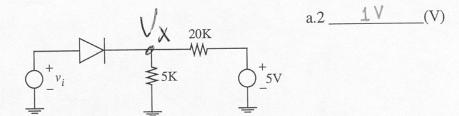
Problem 5 (12 points)

Assume in this problem that all diodes are perfect rectifiers.

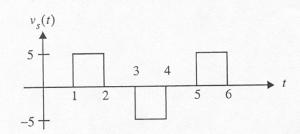
[6 pts.] a) Find V_x when:

a.1
$$v_i = 9V$$

a.2
$$v_i = -5V$$

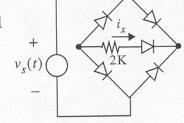


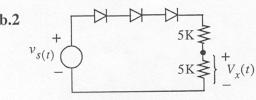
[6 pts.] b)



Sketch the unknown i_x or V_x for the following circuits with the input waveform shown above.

b.1

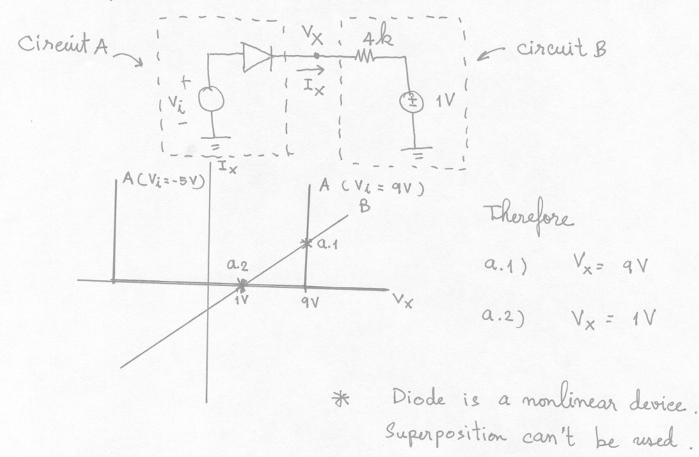




$$V_{x}(t) = \begin{cases} \frac{5k}{5k+5k} & V_{s}(t) = \frac{1}{2}V_{s}(t) & \text{when } V_{s}(t) \neq 0 \\ 0 & \text{when } V_{s}(t) < 0 \text{ gof } 16 \end{cases}$$

Problem 5 Worksheet

a) find Thevenin equivalent of the right side of the circuit (linear part)



- b) b.1) There is no path for the current to flow regardless of the polarity of vscts.

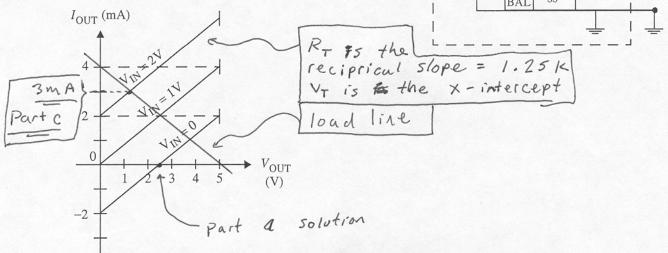
 Therefore, ix is always zero.
 - b.2) Since we assume all diodes are perfect rectifiers, it doesn't matter how many diodes are put in series (in the same direction). Current can flow only when Vsct) is positive. 10 of 16

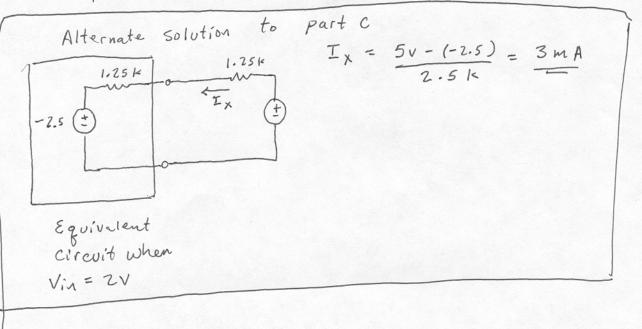
In the lab you encounter a 6-terminal device labeled as follows: One terminal is labeled " V_{SS} ", so you ground it. Another is labeled " V_{DD} ", so you hook it to a +5V supply. Two are labeled "Bal", and not knowing what they might be, you simply ground them. The remaining two terminals are labeled "IN" and "OUT".

So what you now have is essentially a 3-terminal device to study. You proceed to take a series of I-V measurements between $V_{\rm OUT}$ and ground, each set of measurements at one value of $v_{\rm IN}$.

OUT OUT IN VDD IN BAL VSS

You obtain:



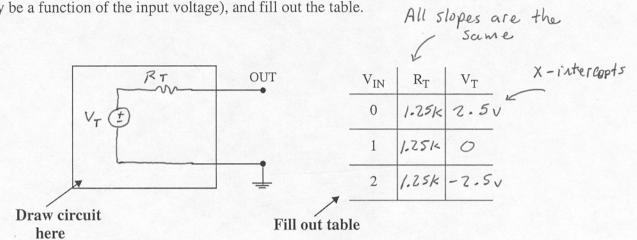


Problem 6 Answer Sheet

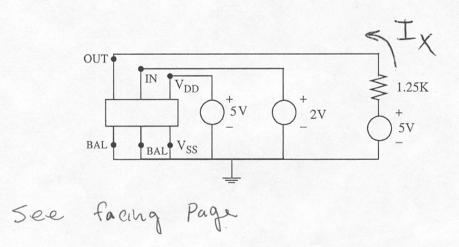
[4 pts.] a) What is the open circuit value of $V_{\rm OUT}$ for $V_{\rm IN}=0$? Check that if $V_{\rm IN}=0$ and $I_{\rm out}=0$ $V_{\rm out}\approx 7.5 \text{ (see facing page.)}$

$$V_{\text{OUT}} = 2.5 \vee$$

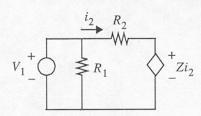
[4 pts.] b) Draw the Thévénin Equivalent circuit of the output for each of the input values given (R_{TH} and V_{TH} may be a function of the input voltage), and fill out the table.



[4 pts.] c) Find I_x (10% accuracy is good enough).

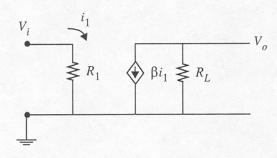


a) Find i_2 in terms of R_1 , R_2 , Z, and V_1 .



$$i_2 = \frac{\bigvee_1}{R_2 + Z}$$

b) Find V_o in terms of R_1 , R_2 , β , and v_i .



$$V_o = \frac{\beta \frac{R_L}{R_I} \cdot V_i}{R_I}$$

c) In the same circuit as (b), if $R_1 = 10$ K, $\beta = 100$, $R_1 = 10$ K, and $V_i = 0.01$ V, what is V_o ?

$$V_o =$$
 $|$ \vee

Problem 8 (14 points)

solution

Scientists at Stanford are excited about discovering a new low-mobility semiconductor "Xanium." The electron mobility in Xanium is 0.5 cm²/V sec and the saturation drift velocity is 10⁶ cm/sec.

They construct a field effect transistor with this material using an insulator of such a thickness that the capacitance per unit area is 10^{-7} conf/cm². The transistor length L is $0.5\mu m$ and the width W is $10\mu m$. They find that the threshold voltage is 2.0 V. Your job is to predict their experimental results.

a) What is the induced charge per unit area in the channel at a gate-source voltage V_{GS} of 5V?

$$Q = \frac{3 \times 10^{-7} F}{cm^2}$$
 (units?)

b) What is the source-drain resistance at $V_{GS} = 1 \text{ V}$?

$$R = \underline{\hspace{1cm}} \Omega$$

c) What is the source-drain resistance at $V_{GS} = 5 \text{ V}$?

$$R = 333.3 \text{ K} \Omega$$

we have $R = R_{\Omega} \left(\frac{L}{\omega} \right) = 333.3K$

d) In a bulk n-type sample of Xanium that is 0.5μm long (in the direction of current flow), how large a voltage would be required to observe velocity saturation? (HINT: Do NOT be too surprised by the answer.)

$$V_{SAT} = \langle v_7 L \rangle = (10^6 \text{cm/s})(0.5 \text{mm})$$

$$0.5 \text{cm}^2/\text{ls}$$

$$0.5 \text{mm} = .5 \times 10^{-4} \text{cm}$$

$$V = \frac{\bigcirc\bigcirc\bigcirc}{\bigcirc}$$
 (V)

e) Would velocity saturation be observed in this transistor (for drain voltages of 5 V or less)?

Y or N N Why? Need Vos > VSAT