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EE140

Midterm Exam

Name: SID:

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Use the following parameters: $V_{t0(NMOS)} = V_{t0(PMOS)} = 0.4v$ $K'_{(NMOS)} = K'_{(PMOS)} = 10mA/V^2 = 10^{-2} A/V^2$ Ff = 0.3v; garma= $1V^{1/2}$; lamda= 0.02

Assume all W/L = 10

1.



What is VB so that $V_{out} = 1.5v$? $V_B =$



a. What is the voltage at VA?

b. What is the maximum swing in the positive direction at Vout?

c. What is the most negative swing at Vout?



a. What is Gm = iout / Vin ?

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b. What is Rout ?

c. What is Av?



For the bipolar: AE = 1 for Q1 AE = 4 for Q2 Vth = 26mV

What is the value of REz that sets I_{out} = 10 μ A ? REZ =



$-1V \le V_{in} \le 1V$

What is the value of **R** which gives the maximum efficiency?

R =

(Hint: Find an expression for R before you plug in numbers)



a. What is Gm?

b. What is Rout?

c. What is Av?

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- a. What is the IDS of transistor M6?
- b. Assume that gm = 0.01, ro = 100k, gmb = 0, for all the transistors. What is Vout / Vin ?