• This is a closed book exam, but you may use your 2 pages of notes.
• Please do all your work on the pages of this exam. Ask if you need extra paper.
• Full credit will be given only when you indicate the source of your answer, such as a table, graph, or calculation.
• Please write your name in the above space
• SPECIAL NOTES:

1. QUESTIONS: Because it is disturbing to others in the exam when we pass by to answer questions, we must ask you to please limit your questions. Re-read the exam question and be sure that the data or equation you need is not already provided.

2. GRAPHS AND FORMULAS PROVIDED: At the back of the exam are several pages of formulas, and data in the form of graphs. Please look for information here rather than asking a question.

☐ Yes, I have looked these over. (Check box)

3. SOME PARTS OF THE EXAM ARE GRADED WITH NO PARTIAL CREDIT. They are noted. You may wish to double check your answers on those parts.

4. DEVICE PROPERTIES: Unless otherwise specified, assume n-channel transistors have $V_{tn} = 0.5 \text{ V}, \mu C_{ox} = 50 \mu \text{A/V}^2$, and p-channel transistors have $V_{tp} = -0.5 \text{ V}, \mu C_{ox} = 25 \mu \text{A/V}^2$. In small-signal calculations assume a lambda of 0.05 for both types. Assume bipolar transistors have beta of 100 and $I_{ES}$ of 10-15 A (both nnp and pnp transistors). In small-signal calculations assume the Early voltage is 20 V. You are to ignore lambda and Early voltage in biasing calculations.
Problem 1  (15 pts., NO Partial Credit)

You buy three voltage amplifiers with the following specifications: $R_{in} = 10^5$, $R_{out} = 10^5$, $A = 10$. You hook them up in series (i.e., output 1 = input 2, etc.), and place them in a black box. You are going to sell this box as a transconductance amplifier. What are the specifications of the transconductance amplifier?

![Diagram of three amplifiers in series](image)

- $G_m =$
- $R_{in} =$
- $R_{out} =$

Problem 2  (15 points)

a. Find the phasor representing the ratio of short-circuit output current to input current for the following circuit:

![Circuit diagram](image)

$$A_1 = \frac{I_{out}}{I_{in}} = \frac{3}{2}$$

b. What is the general shape of the frequency response? Sketch magnitude vs frequency on dB scale provided on pg. 2.
Problem 3  (20 points)

Sketch the Bode plot on the graphs provided for the following function. Please show your work. (Neatness and clarity are important.) Only straight-line approximations are wanted.

\[
F = \frac{200j\omega \times \left(1 + \frac{j\omega}{200}\right)}{(2 + j\omega)(1 + 100j\omega)(1 + 0.0002j\omega)}
\]
Problem 4  (25 points)

In the circuit below, the reference node $A$ is at a potential of 1.5 V. The $W/L$ ratios for the n-channel devices are chosen as: $(W/L)_2 = 2(W/L)_1$. For the p-channel devices $(W/L)_4 = 10(W/L)_3$. Also, $\left(\frac{W}{L}\right)_3 = 4\left(\frac{W}{L}\right)_1$.

a. Find $W/L$ for n-channel device MN1 (to produce the required 1.5 V at node $A$).

b. Find the drain current of MN2 and the node voltage $V_Y$.

c. Find the value of $R_4$ needed to produce a voltage of 3 V at node $Z$.

\[ V_{DD} = 6 \text{ V} \]

\[ \begin{align*}
(W/L)_1 &= \underline{\quad} \\
I_D &= \underline{\quad} \\
V_Y &= \underline{\quad} \\
R_4 &= \underline{\quad}
\end{align*} \]
Problem 5 (25 points)

a) You are given one “power nMOS” transistor and need to construct a transistor circuit to drive an 8 ohm speaker through a coupling capacitor $C_X$. (The “power nMOS” transistor has a 1V threshold, a $W/L$ of $10^4$, and $\mu C_{ox} = 50 \mu A/V^2$. The source is connected to the body internally.) Your circuit should have a voltage gain of about 0.5 at higher frequencies (that is, at frequencies high enough that the impedance of $C_X$ is negligible). (Note that $C_X$ must be quite large to have a good low frequency response.) The bias current source you have available is essentially ideal (i.e., infinite parallel resistance). You choose the circuit below.

![Circuit Diagram]

a) Draw the small-signal model for this amplifier in the box opposite, ignoring all internal device capacitances and parasitic resistances.

b) Solve for the “mid-band” gain $A_M$ (formula in terms of circuit and device parameters). The mid-band gain is a real quantity. It is the gain at frequencies high enough that coupling capacitors act as shorts, yet the frequency is low enough that capacitors to ground are negligible.

$$A_M = \phantom{0}$$

c) Find the mid-band gain of the circuit $A_M$ at a bias current of $I_{BIAS} = 10 \text{ mA}$.

$$A_M = \phantom{0}$$
Problem 5 Answer and Worksheet

a.

![Small-signal Model Diagram]

b.

c.
Appendix A:
Appendix B:
Appendix C: Formulas

(EQ 2.7) \[ n_o \cdot p_o = n_i^2(T) \]

(EQ 2.34) \[ v_{dn} = -\mu_n E \]

(EQ 2.55) \[ J_n = J_n^{dr} + J_n^{diff} = qn\mu_n E + qD_n \frac{dn}{dx} \]

(EQ 2.56) \[ J_p = J_p^{dr} + J_p^{diff} = qp\mu_p E - qD_p \frac{dp}{dx} \]

(EQ 2.67) \[ R = \left( \frac{1}{qN_d\mu_n} \right) \left( \frac{L}{W} \right) = R_a \left( \frac{L}{W} \right) = R_a N_a \]

(EQ 3.1) \[ \frac{dE}{dx} = \frac{\rho}{\varepsilon} \]

(EQ 3.4) \[ E(x) = -\frac{d\phi(x)}{dx} \]

(EQ 3.56) \[ X_{do} = x_{no} + x_{po} = \sqrt{\frac{2\varepsilon_s\phi_B}{q}} \left( \frac{1}{N_a} + \frac{1}{N_d} \right) \]

(EQ 3.88) \[ V_{FB} = -\left( \phi_{n+} - \phi_p \right) \]

(EQ 3.89) \[ Q_G(V_{GB} = V_{FB}) = 0 \]

(EQ 3.95) \[ V_{Tn} = V_{FB} - \frac{1}{C_{ox}} \sqrt{\frac{2q\varepsilon_s N_a (-2\phi_p)}{N_d}} \]

(EQ 4.18) \[ V_{DS_{SAT}} = V_{GS} - V_{Tn} \]

(EQ 4.19) \[ I_{DS_{SAT}} = \left( \frac{W}{2L} \right) \mu_n C_{ox} (V_{GS} - V_{Tn})^2 = \left( \frac{W}{2L} \right) \mu_n C_{ox} V_{DS_{SAT}}^2 \]

\[ I_D = 0 \text{ A} \]

\[ V_{Tn} = V_{TOn} + \gamma_n \left( \sqrt{-V_{BS} - 2\phi_p} - \sqrt{-2\phi_p} \right) \]

\[ g_m = \left( \frac{W}{L} \right) \mu_n C_{ox} (V_{GS} - V_{Tn}) = \sqrt{\frac{2}{W}} \mu_n C_{ox} I_D \]
\( \phi_B = V_{th} \ln\left( \frac{p_{po}}{p_{no}} \right) \) and \( \phi_B = V_{th} \ln\left( \frac{n_{no}}{n_{po}} \right) \)

(EQ 6.22) \( p_n(x_n) = p_{no} e^{V_D/V_{th}} \) and \( n_p(-x_p) = n_{po} e^{V_D/V_{th}} \)

(EQ 6.30) \( J = qn_i^2 \left( \frac{D_p}{N_a W_n} + \frac{D_n}{N_a W_p} \right) (e^{V_D/V_{th}} - 1) \)

(EQ 6.31) \( I_D = qn_i^2 A \left( \frac{D_p}{N_a W_n} + \frac{D_n}{N_a W_p} \right) (e^{V_D/V_{th}} - 1) = I_o (e^{V_D/V_{th}} - 1) \)

(EQ 6.48) \( C_j = \frac{C_{jo}}{\sqrt{1 - V_D/\phi_B}} \)

Appendix D: Values

\( q = 1.6 \times 10^{-19} C \)

\( V_t = kT/q = 0.026 \text{ V} \)

\( \varepsilon_0 = 8.85 \times 10^{-14} \text{ F/cm} \)

\( \varepsilon_{\text{oxide}} = 3.9 \varepsilon_0 \)

\( \varepsilon_{Si} = 11.7 \varepsilon_0 \)

Appendix E: SIA unit multipliers:

\( M = 10^6 \)

\( K = 10^3 \)

\( m = 10^{-3} \)

\( \mu = 10^{-6} \)

\( n = 10^{-9} \)

\( p = 10^{-12} \)

\( f = 10^{-15} \)