# University of California, Berkeley <br> College of Engineering <br> Dept. of Electrical Engineering and Computer Sciences 

EE 105 Midterm II

## Guidelines

Closed book and notes; one $8.5 " \times 11^{\prime \prime}$ page (both sides) of your own notes is allowed. You may use a calculator.
Do not unstaple the exam.
Show all your work and reasoning on the exam in order to receive full or partial credit.

Score

| Problem | Points <br> Possible | Score |
| :---: | :---: | :---: |
| 1 | 15 |  |
| 2 | 18 |  |
| 3 | 17 |  |
| Total | 50 |  |

1. Phasor Analysis


This circuit is a model of a small-signal amplifier. The element values are:
Source resistance $R_{s}=20 \mathrm{k} \Omega$
Input resistance $R_{\text {in }}=5 \mathrm{k} \Omega$
Input capacitance $C_{\text {in }}=25 \mathrm{pF}$
Current gain $A_{i}^{\prime}=100$
Load resistance $R_{L}=25 \mathrm{k} \Omega$
(a) [4 pts.] Find the phasor current $I^{\prime}$ in terms of the phasor source voltage $V_{s}$ and $R_{s}$, $R_{i n}$, and $C_{i n}$. There is no need to substitute numerical values for the elements.
(b) [4 pts.] Sketch the magnitude of $V_{\text {out }} / V_{s}$ using straight-line approximations.

(c) [4 pts.] Sketch the phase of $V_{\text {out }} / V_{s}$ using straight-line approximations.

(d) [3 pts.] If the phasor source voltage $\mathrm{V}_{s}=(2.5 \mathrm{mV}) \mathrm{e}^{\mathrm{j} 0^{\circ}}$ at a frequency of $10^{4} \mathrm{rad} / \mathrm{sec}$, what is $v_{\text {out }}(t)$ based on the information in your Bode plots?
2. MOSFET single stage amplifier [18 pts.]

(a) [2 pts.] Find the numerical value of the bias current $I_{\text {BIAS }}$ such that the DC output current $I_{\text {OUT }}=0 \mathrm{~A}$. The effect of channel-length modulation can be neglected for this part.
(b) [3 pts.] Find the DC reverse bias voltage across the photodiode, $V_{R}$, in Volts.
(c) [3 pts.] The photodiode's small-signal model is a current source $i_{s}(t)$ in parallel with a source resistance of $R_{\mathrm{s}}=20 \mathrm{k} \Omega$. (i) label the two-port model as CS, CG, or CD and (ii) fill in the block with the circuit elements for the correct amplifier type (transconductance, current, or voltage). There is no need to evaluate the two-port model parameters for this part.

(d) [3 pts.] What is the numerical value of the input resistance $R_{\text {in }}$ of this amplifier?
(e) [3 pts.] What is the numerical value of the output resistance $R_{\text {out }}$ of this amplifier?
(f) [4 pts.] If the photodiode's source current is $i_{s}=15 \mu \mathrm{~A}$, find the numerical value of the output current $i_{\text {out }}$ in $\mu \mathrm{A}$.
3. Voltage supply [17 points]

(a) [3 pts.] Find the numerical value of $v_{A}$ in Volts. Neglect the backgate effect and channel-length modulation.
(b) [4 pts.] Find the width of transistor $M_{3}(\mathrm{in} \mu \mathrm{m})$ such that the output voltage $v_{\text {OUT }}$ $=1.5 \mathrm{~V}$. You can neglect channel-length modulation and the backgate effect. If you couldn't solve part (a), you can assume that $v_{A}=.75 \mathrm{~V}$ for this part.
(c) [4 pts.] Draw the small-signal circuit for finding the Thévenin resistance $R_{s}$ looking into the output voltage terminals (see schematic on previous page). You can ignore the backgate transconductance for this part. There is no need to solve for $R_{s}$.
(d) [3 pts.] Find the numerical value of the Thévenin resistance $R_{s}$ looking into the output voltage terminals. If you couldn't solve part (b), you can assume that the $W_{3}=20 \mu \mathrm{~m}$ for this part. You can also ignore the backgate transconductance for this part; you should also make judicious approximations.
(e) [3 pts.] The bulk terminal of $M_{3}$ is connected to ground. Using your results from part (b) for the width of $M_{3}$, what is the numerical value of the output voltage $v_{\text {OUT }}$ in Volts? Ignore channel-length modulation. If you couldn't solve part (b), you can assume that the $W_{3}=20 \mu \mathrm{~m}$ for this part.


