## Microelectronic Devices and Circuits - EECS 105 <br> Spring 2003 Midterm 1 <br> Professor Costas J. Spanos

MOS Device Data (otherwise indicated on the particular problem)
(you may not have to use all of these...)
$\mu_{\mathrm{n}} \mathrm{C}_{\mathrm{ox}}=50 \mu \mathrm{~A} / \mathrm{V}^{2}, \mu_{\mathrm{p}} \mathrm{C}_{\mathrm{ox}}=25 \mu \mathrm{~A} / \mathrm{V}^{2}, \mathrm{~V}_{\mathrm{Tn}}=-\mathrm{V}_{\mathrm{Tp}}=1 \mathrm{~V}, \mathrm{Lmin}=1 \mu \mathrm{~m} . \mathrm{V}_{\mathrm{BS}}=0$.
$\lambda_{\mathrm{n}}=\lambda_{\mathrm{p}}=0.1 \mathrm{~V}^{-1}$ when $\mathrm{L}=1 \mu \mathrm{~m}$, and it is otherwise proportional to $1 / \mathrm{L}$
$\mathrm{C}_{\mathrm{ox}}=2.3 \mathrm{fF} / \mu \mathrm{m}^{2}$.
Also: $\varepsilon_{0}=8.8510^{-14} \mathrm{~F} / \mathrm{cm}, \varepsilon_{\mathrm{ox}}=3.9 \varepsilon_{0}$ and $\varepsilon_{\mathrm{si}}=11.7 \varepsilon_{0}$.

Problem 1 of 4: Answer each question briefly and clearly. (5 pts each, 40 total)
1.1 Assume a diffused resistor with a fixed thickness. What happens to the Sheet Resistance (Rs) if the doping level doubles (assuming that there is only a single dopant and that mobility does not change)?

- ... stays the same
$\square \quad$... doubles
- ... is reduced by $50 \%$

■ ... none of the above
1.2 Find the "built-in potential" $\varphi_{B}$ of a junction that has $N_{A}=10^{16} / \mathrm{cm}^{3}$ and $N_{D}=$ $10^{17} / \mathrm{cm}$.
1.3 Place check marks where appropriate to indicate the correct region of operation, assuming that $\mathrm{V}_{\mathrm{Tn}}=-\mathrm{V}_{\mathrm{Tp}}=1 \mathrm{~V}$.

| Transistor | $\mathbf{V}_{\text {GS }}$ | $\mathbf{V}_{\text {DS }}$ | $\mathbf{V}_{\text {BS }}$ | Off | Triode | Saturation |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| NMOS | 2 | 2 | 0 |  |  |  |
| NMOS | 2 | $\mathbf{0}$ | $\mathbf{0 . 5}$ | $\mathbf{0}$ |  |  |
| PMOS | -2 | -2 | 0 |  |  |  |
| PMOS | 2 | 0.5 | 0 |  |  |  |

1.4 Choose the most appropriate answer.

A negatively charged "depletion" region in doped Si is characterized by ...

- ... an abundance of electrons and negative ions
... too many electrons, too few holes
... a severe imbalance between negative ions and holes
... the complete depletion of charge density
1.5 We have a 2-terminal device that accumulates charge as a function of voltage according to the equation: $\mathrm{Q}=5 \mathrm{~V}^{2}+3 \mathrm{~V}+2$, where Q is in pico Cb and V is in volts. Find the small signal capacitance of this device in pF at 2 V .
1.6 Write the expression of a transfer function that corresponds to the Bode plot drawn.

1.7 The value of a voltage source is $5+0.02 \cos \left(10^{6} t+45^{\circ}\right)$ Volts. Please complete the list below:
$\mathrm{V}_{\mathrm{s}}=$
$\mathrm{v}_{\mathrm{s}}=$
$\mathrm{v}_{\mathrm{s}}=$
1.8 For the n-channel MOS transistor shown below, please consider the two ends of the channel (near the source and near the drain) and indicate whether or not each must be inverted so that this device is in saturation.


Problem 2 of 4: Answer each question briefly and clearly. (20 points) The C/V plot below was taken by "scanning" the gate-to-bulk voltage of an MOS structure. We know (from other measurements) that $\varphi_{\mathrm{n}+}=0.55 \mathrm{~V}$ and $C_{o x}=2.3 \mathrm{fF} / \mu \mathrm{m}^{2}$. Also, $\varepsilon_{0}=8.8510^{-14} \mathrm{~F} / \mathrm{cm}, \varepsilon_{\mathrm{ox}}=3.9 \varepsilon_{0}$ and $\varepsilon_{\mathrm{si}}=11.7 \varepsilon_{0}$. Use the graph to calculate:

2.1 The oxide thickness $t_{o x}$.

|  | expression |
| :--- | :--- |
| $t_{o x}=$ |  |
|  |  |

2.2 The maximum depth of the depletion region (What is the $V_{G B}$ voltage that yields the maximum depth of the depletion region?).
expression
value and units

| $x_{\text {dmax }}=$ |  |
| :--- | :--- |
|  |  |

$V_{G B}$ when maximum depletion depth is achieved $=$
2.3 The doping concentration of acceptors $\left(\mathrm{N}_{\mathrm{A}}\right)$ in the channel.

|  | expression |
| :--- | :--- |
| $N_{A}=$ |  |
|  |  |

2.4 The charge density of the inversion layer (in $\mathrm{Cb} / \mathrm{cm}^{2}$ ) when $V_{G B}$ is 2 V .
expression
value and units

| $Q_{i}=$ |  |
| :--- | :--- |

Problem 3 of 4: Answer each question briefly and clearly. (20 points)
Consider the circuit below. Assume $\mathrm{R}_{1}=1 \mathrm{M} \Omega, \mathrm{R}_{2}=100 \Omega$, and $\mathrm{C}=1 \mathrm{pF}$.

3.1 Write the transfer function $v_{o u t} / v_{i n}$.
3.2 Calculate the gain at DC , the gain at extremely high frequencies ( $\omega=$ infinity), and the numerical values of any poles and/or zeros that this function has.

| expression |  |
| :--- | :--- |
| Gain $_{\text {at DC }}=$ | value |
| Gain $_{\text {at } \omega=\text { infinity }}=$ |  |
| Pole $1=$ |  |
| Pole $2=$ |  |
| $\ldots$ |  |
| Zero $1=$ |  |
| Zero $2=$ |  |
| $\ldots$ |  |

Problem 4 of 4: Answer each question briefly and clearly. (20 points)
You are given an NMOS transistor, connected as shown.

4.1 Find an expression for W so that the output voltage $\mathrm{V}_{\text {out }}$ is 2.5 V . Assume $\lambda_{\mathrm{n}}=0.1 \mathrm{~V}^{-1}$ when $\mathrm{L}=1 \mu \mathrm{~m}$, and it is otherwise proportional to $1 / \mathrm{L}$.
expression
$W=$
4.2 Assume $\mathrm{R}=10 \mathrm{k} \Omega, \mu_{\mathrm{n}} \mathrm{C}_{\mathrm{ox}}=50 \mu \mathrm{~A} / \mathrm{V}^{2}, \mathrm{~V}_{\mathrm{Tn}}=1 \mathrm{~V}, \lambda_{\mathrm{n}}=0.1 \mathrm{~V}^{-1}$ when $\mathrm{L}=1 \mu \mathrm{~m}$, and it is otherwise proportional to $1 / \mathrm{L}$. Find $\mathrm{W} / \mathrm{L}$ when L is $2 \mu \mathrm{~m}$, and when L is $4 \mu \mathrm{~m}$.
value
$\mathrm{W} / \mathrm{L}_{\text {when } \mathrm{L}}=2 \mu \mathrm{~m}=$
$\mathrm{W} / \mathrm{L}_{\text {when } \mathrm{L}}=4 \mu \mathrm{~m}=$

