### cs61c, Spring 1998 Midterm #1 Professor Clancy

## Problem #1 (7 points, 14 minutes)

In lab assignment 4, you wrote a function that returned the contents of the various fields of a MIPS I-format instruction of the bits is as follows.

The first 8 bits are the op code. The next 4 bits are the register to be modified by the instruction. The last 20 bits are the immediate operand, in 1's complement.

Thus the equivalent to the MAL instruction, addi \$10,-2 might appear in hexadec 94 af ff fd

if the op code for the addi instruction were 94 base 16.

On the next page, write a MAL function SplitIFormat that returns the contents of the register and immediate fields of a

void SplitIFormat (int instr, int \*register, int \*immediate);

If written in C++, its prototype would be void SplitIFormat (int instr, int &register, int &immediate);

Follow the conventions described in class and in lab and homework assignment 6 for passing arguments and managing

## Problem #2 (7 points, 13 minutes)

Part a Translate the following MAL program segment to TAL. You may use either names or numbers for the registers.

	li	\$t1,-5
loop:	sub	\$t1,\$t1,3
	bgt	\$t1,\$a1,loop

Equivalent TAL segment:

#### Part b

Your answer to part a should include a branch instruction. Translate this branch instruction to machine language by fil

			-	-	-	-	-	Concession of the	_	-	-	-	-	-	-	and the second second	_	-	_	No. of Concession, name	-	and the second se	No.	-	-	and the second second	-		-	-	_	-
-																																
																													1			
						1																										
	-	-	-	A summer		Acres 1	-	_	and in case of		-	the second second	the state of the s	-	-	_	_	-	-	the second second		-			And the other Designation of the local division of the local divis		and the second	-	-	-	-	

# Problem #3 (7 points, 10 minutes)

```
Consider a list with nodes defined in C or C++ as follows.
    struct ListNode {
        char name[6];
        int code[3];
        struct ListNode* next; /* ListNode* next in C++ */
    };
```

The diagram below, not drawn to scale, gives an example of such a list.



Part a

Assume that register \$a1 contains a pointer to the first node of the list. Write MAL code that loads \$s2 with the second

Part b

Again, assume that register \$a1 contains a points to the first node of the list. Write MAL code that loads \$s2 with the f

## Problem #4 (7 points, 12 minutes)

Consider the following C functions that check if one string contains another as

```
int ContainsAsSubstring (char *s1, char *s2) {
      if (*s2 == '\0') {
                               /* if string 2 has run out, */
            return 1;
                               /* it's a substring of string 1
      /* string 2 isn't a substring c
            return 0;
      } else if (StartsWith (s1, s2)) {
            return 1;
      } else {
            return ContainsAsSubstring (s1+1, s2);
      }
int StartsWith (char *s1, char *s2) {
      if (*s2 == '\0') {
                               /* any string starts with the e
            return 1;
      return 0;
      } else if (*s1 != *s2) {
            return 0;
      } else {
            return ContainsAsString (s1+1, s2+1);
      }
```

}

Some examples of how ContainsAsSubstring behaves are listed below.

String 1	Str	ring 2	result of ContainsAsSubstring
"abcde"	"abc"	1	
"xyabc"	"abc"	1	
"axbc"	"ab"	Θ	
"xy"	"abc"	Θ	

Fill in the missing code in the MAL implementation of ContainsAsSubstring below. (Don't worry about StartsWith.) Y registers and the system stack. You may assume that neither argument pointer is null.

#check base cases

```
beqz $t1, returnTrue
        begz $t0,returnFalse
                                #does string 1 start with string 2?
       move $s0,$a0
       move $s1,$a1
        jal
             StartsWith
        bnez $v0, returnTrue
                                         #no match; make recursive call
               $a0,$s0,1
       add
       move $a1,$s1
                ContainsAsSubstring
        jal
                 return
        i
returnTrue:
       #prepare to return 1
              return
        j
returnFalse:
       #prepare to return 0
return:
       # restore registers and return
```

Posted by HKN (Electrical Engineering and Computer Science Honor Society) University of California at Berkeley If you have any questions about these online exams please contact <u>examfile@hkn.eecs.berkeley.edu.</u>