## UC Berkeley : CS61C (Garcia & Lustig) : Midterm Part 2 : 2014-10-17

cs61c-\_\_ Name (first last) SID Login ← Name of person on left (or aisle) Name of person on right (or aisle)  $\rightarrow$ Question 1: Hit or Miss, it's AMAT-er of Performance (24 min, 19 pts) You are given a single 8 KiB direct-mapped cache with 256 B blocks and a write-back policy. Assume a 32-bit address space and byte-addressed memory. Show your work. a) Label the fields below as Tag, Index, and Offset, and give the # of bits for each. (e.g. Tag:5) (3 pt) T:19 1:5 O: 8 31 0 b) Assume that  $\mathbf{a}_1 \mathbf{o}_1$  is at the beginning of a cache block and that the cache is empty to begin with. Answer the questions below based on the following code. char A[32768]; // 32768 is 2^15 for (int i = 0; i < 32768; i+=64) A[i] = '\0'; // 1<sup>st</sup> LOOP for (int i = 0; i < 32768; i+=64) A[i & 1] = '\0';  $// 2^{nd} LOOP$ What is the exact hit rate for the 1<sup>st</sup> LOOP? Leave your answer as a fraction. (3 pt) i.

(2^9 - 1)/(2^9) = 511/512

What is the exact hit rate on the  $2^{nd}$  LOOP? Leave your answer as a fraction. (3 pt)

3/4

Which of the following types of misses occur in part (i)? (circle only one) (2 pt)

Conflict

c) Suppose we run some code and our L1 cache hits in 2 cycles and has a local hit rate of 80%. Main memory always hits in 50 cycles. What is the AMAT of this system? (3 pt)

## 2 + 0.2 \* 50 = 12 cycles

d) Match each term with <u>all</u> letters on the right that apply (you may have multiple per blank). (5 pt)

Interpreter \_\_\_\_\_F Compiler \_\_\_\_\_A Assembler \_\_\_\_\_C, E Linker \_\_\_\_\_D Loader \_\_\_\_\_B

ii.

iii.

Compulsory

- A) Converts C code into Assembly
- B) Copies program binary to memory to prepare for execution

Neither

→Both ←

- C) Converts from MAL to TAL
- D) Computes the jump address for a jal instruction
- E) Computes the offset for a beq instruction
- F) Directly executes a program written in source code

## Question 2: What's that funky smell?! Oh yeah, it's potpourri ... (26 min, 20 pts)

- a) We examine a word in memory and find that it holds the value **0x20707coo**.
  - i. If it were a TAL MIPS instruction, what would it be? (Leave immediates and jump addresses in hexadecimal form.) *Show your work.* (2 pt)



Positive Number + Positive Number, overflows if result is \_\_\_\_\_.<0

Negative Number + Negative Number, overflows if result is \_\_\_\_\_

- .≥0
- d) What is the decimal value of the int16\_t number 0x8000? How does it relate to the advantages of two's complement over one's complement? (2 pt)

The decimal value is -2<sup>15</sup>. Two's complement has only one zero, which means that one additional negative integer can be represented.