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EE147/247A Final, Fall 2016

NO CALCULATORS, CELL PHONES, or other electronics allowed. Show your work, and put final answers in the boxes provided. Use proper units in all answers.

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1. [17] True/False (circle one) 1pt each
   a. **True / False** SEMulator3D allows you to mesh and perform mechanical/electrical simulations on structures you design in layout (True/False)
   b. **True / False** A "LoadPatchNodes" boundary condition in CoventorWare is used to fix anchors during a simulation
   c. **True / False** The resistivity of silicon can be changed over many orders of magnitude just by adding less than 1% impurities.
   d. **True / False** In a silicon strain gauge, the fractional change in resistance ($\delta R/R$) is much greater than the fractional change in length ($\delta L/L$)
   e. **True / False** If you decrease all of the dimensions in an electrostatic actuator by 10 but keep the voltage the same, the force will go up by a factor of 100.
   f. **True / False** If you keep the dimensions of an electrostatic actuator the same, but increase the voltage by a factor of 10, the force will go up by a factor of 100.
   g. **True / False** If you change the sign of the voltage across a comb drive the force changes direction.

   h. **True / False** LPCVD polysilicon is conformal

   i. **True / False** LPCVD PSG is conformal

   j. **True / False** Evaporated aluminum is conformal

   k. **True / False** Sputtered aluminum is conformal

   l. **True / False** Plasma etching with SF6 is usually isotropic

   m. **True / False** Reactive ion etching with CF4 is usually isotropic

   n. **True / False** In RIE, sidewalls are protected by C4F8 cracked in the plasma.

   o. **True / False** In DRIE, vertical anisotropy is due to fluorine ions

   p. **True / False** You can use liftoff to pattern evaporated aluminum

   q. **True / False** You can use liftoff to pattern thermal oxide

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2. [24] Short answer, 2pts each. Give a brief description in support of your answer (a sentence fragment is fine).
   a. When doing layout, if I draw a 10x10um2 rectangle on a layer called METAL1, would you expect to see a 10x10um2 square of metal on the wafer in that area, or a 10x10um2 hole?

   b. When doing layout, if I draw a 10x10um2 rectangle on a layer called CONTACT that will be used to pattern an LTO, would you expect to see a 10x10um2 square of oxide on the wafer in that area, or a 10x10um2 hole?

   c. Why would the capacitance be larger in a CoventorWare simulation than the simple parallel plate hand calculation?

   d. If you wanted to accurately model the fabrication details of a new process you came up with, would you use SEMulator3D or CoventorWare? Why?

   e. List three MEMS applications where more than one billion dollars in MEMS are sold each year.

   f. The macroscopic breakdown field for air is roughly 3V/um. Why doesn't the air break down when we run 2um polysilicon gaps at 100V? (ideally give both a name and a short explanation)

   g. Can we put 100V across 2um gaps with all materials (like metals)? Why or why not?

   h. Atoms have discrete energy levels. Crystals have bands of energies. Why?

   i. Why do we anneal our wafers after ion implantation?

   j. During annealing after ion implantation, the surface concentration typically first goes up, then down. Why?

   k. Give two reasons why we pump down to very low pressure in an aluminum evaporator.

   l. How do we typically dope polysilicon for MEMS devices?

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3. [4] Write an equation that would let you calculate the angle between the (110) plane and all planes in the {100} family in a silicon crystal. Solve it for all possible angles.

4. [3] List three isotropic silicon etches, one gas, one liquid, one plasma

5. [3] List three anisotropic silicon etches, at least one liquid, at least one plasma

6. [4] You stretch (put a positive strain on) a piece of silicon, and the resistance goes down.
   a. Can this happen with N-type silicon? How/why?

   b. Can this happen with P-type silicon? How/why?

7. [2] Your friend from Stanford deposited nitride, PSG, and polysilicon on a bare wafer, annealed it, and then dipped it in HF. She thinks that the HF must be dirty because the wafer looks awful. What do you tell her?

8. [2] After fixing her previous problem, with a new wafer she etches and releases a variety of long beams using a single-mask design. The beams all curl out of plane. What do you tell her?

9. [2] Your friend from MIT makes fun of your SOIMUMPS comb-drive calculations because you ignored the fringing field. He says that your calculations will be wrong by a factor of 2. What do you tell him?

10. [9] You have developed a new displacement sensor using a cantilever with a piezoresistor in a Wheatstone bridge. The bridge resistance is 1kΩ. The stiffness of the cantilever is 40 N/m. The damping coefficient is 0.025Ns/m at atmospheric pressure, which gives a very low Q, close to 1.
    a. What is the average displacement noise of the cantilever over all frequencies? (formula and number)

    b. What is the power spectral density of the noise force on the cantilever? (formula and number)

    c. What is the displacement noise in a 100 Hz bandwidth at low frequency? (formula and number)

    d. If the damping decreases by four orders of magnitude at low pressure,
       i. how does the average noise displacement of the cantilever change? (e.g. increase 2x, decrease 10%, etc.)

       ii. how does the power spectral density of the noise force change?

       iii. how does the displacement noise in a 100 Hz bandwidth at low frequency change?

11. [4] You have made a MEMS resonator with a spring constant of 1 N/m and a Q of 100. You apply a force with an amplitude of 1nN and various frequencies. What is the amplitude of the displacement when the force is applied
    a. At $\omega << \omega_n$

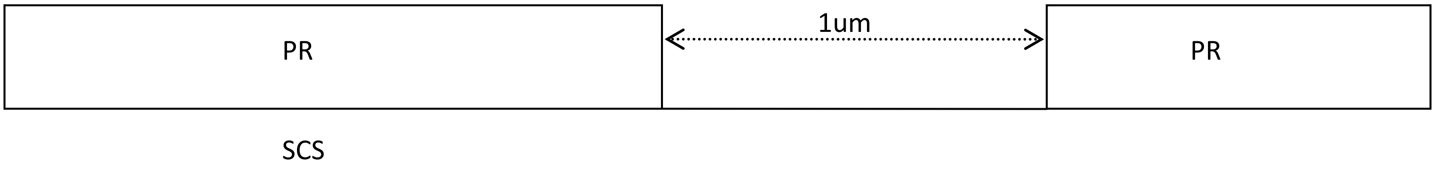
    b. At $\omega = \omega_n$

    c. At $\omega = 2\ \omega_n$

    d. At $\omega = 10\ \omega_n$

12. [8] Sketch a CF_4 polysilicon reactive ion etcher, showing gas inlet, pump, power supply wiring, wafer, and ground. Describe two etching mechanisms and one deposition mechanism that happen during etching (chemical, kinetic, PECVD, ions, who, what, where, etc).

13. [5] You have a silicon wafer coated with photoresist. There is a 1 micron hole in the photoresist. Draw the cross-section after the wafer has been exposed to two and a half cycles in a DRIE etcher (etch, dep; etch, dep; etch). Assume 0.5um per etch step. Label any materials other than silicon and photoresist.



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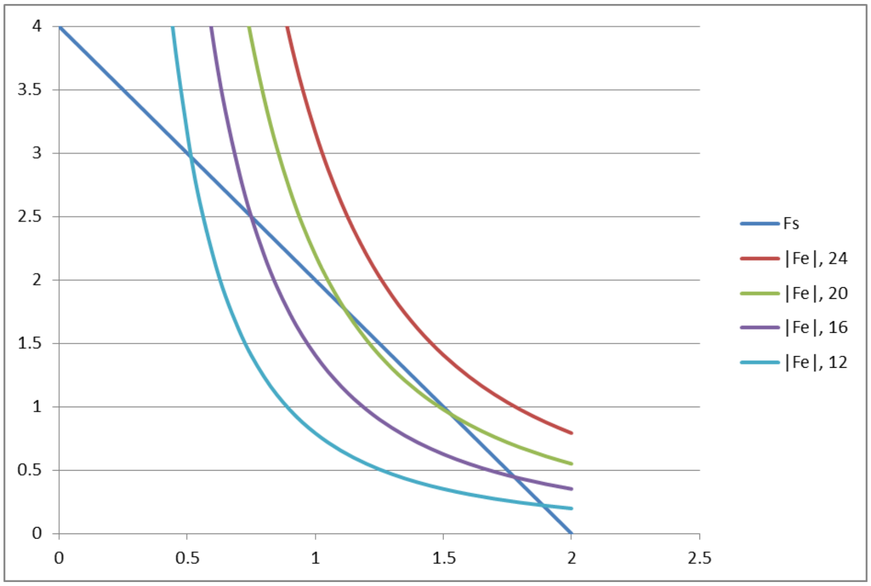
14. [8] The figure below shows the spring force and the magnitude of the electrostatic force for a gap-closing relay actuator running at four different applied voltages: 12, 16, 20, and 24 volts. The horizontal axis is in microns, the vertical in micronewtons. The initial gap is 2um, and there is a gap stop (relay contacts) at 1um.

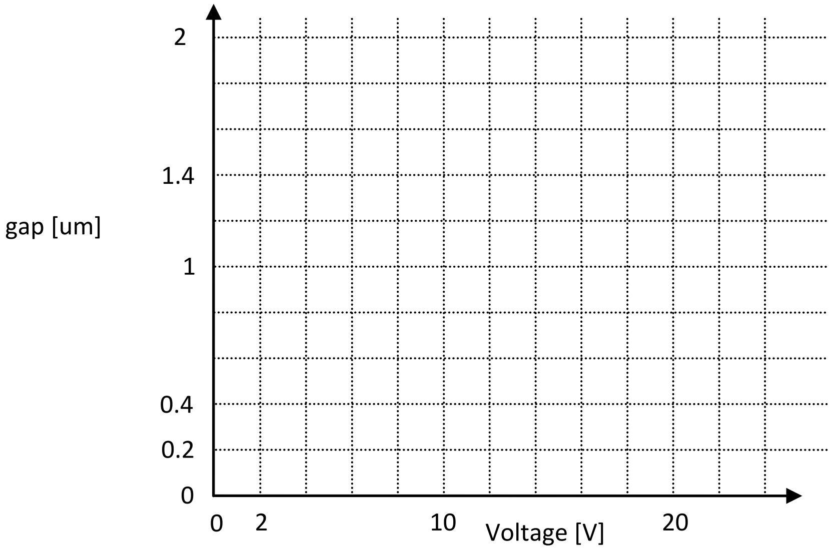


a. Estimate the pull-in voltage

b. Estimate the pull-out voltage

c. Estimate the force on the relay contacts if 24V is applied

d. Carefully sketch the displacement of the actuator as the voltage is increased from 0 to 24V, and then decreased from 24 to 0. Try to use specific points from the graph above. No sloppy sketches!



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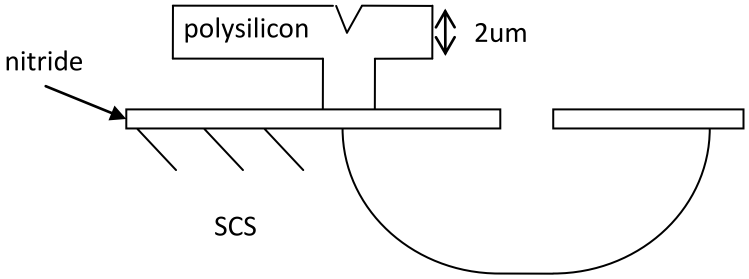
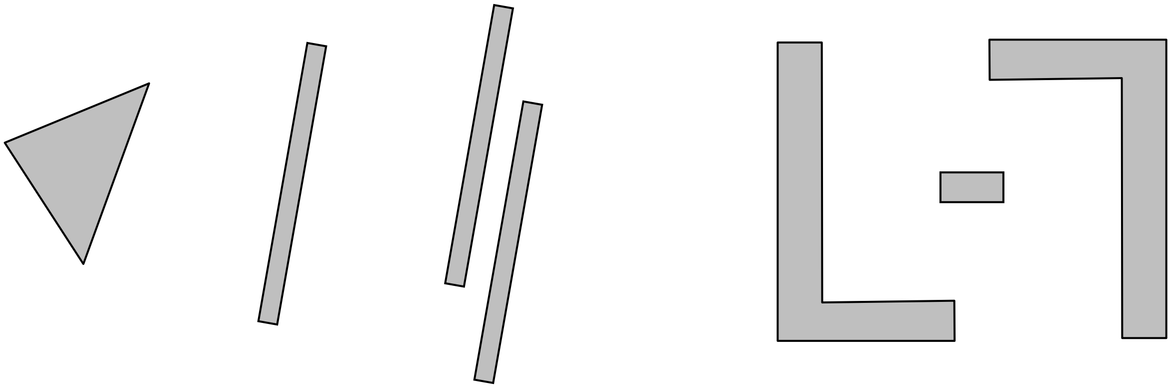
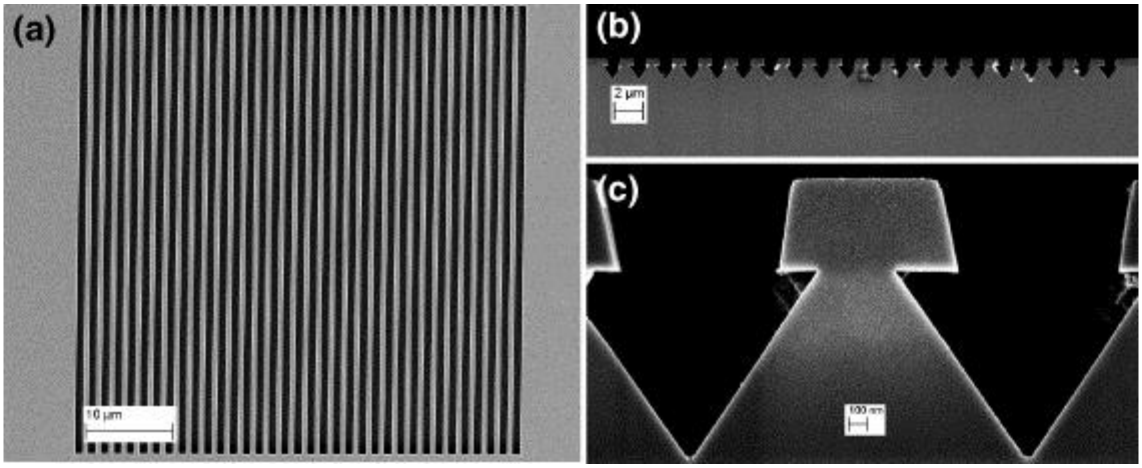
14. [6] Write down a process flow that would let me make the following cross-section.



15. [4] A (100) wafer coated with silicon dioxide has the following regions opened to the silicon surface. The wafer is dropped in a KOH etch and the etch runs until only 111 planes are exposed. What is the outline of the etched regions under the silicon dioxide (i.e. where is the region where the SiO2 will not be supported by silicon)? Assume that this page is oriented with the wafer flat.



16. [6] The structure below has a layer of silicon nitride on (100) single-crystal silicon. On the left is a top view, on the right is a cross-section. The scale bar on the left is 10um, on the right is 100nm.



Kemiktarak et. al “Cavity optomechanics with sub-wavelength grating mirrors”, NJP V14, Dec 2012

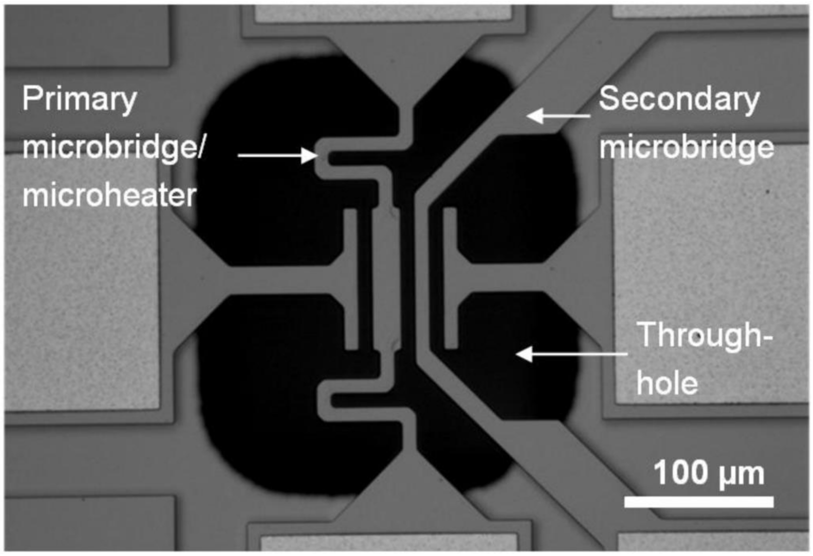
a) Write down a simple process flow that would create this device

b) Are the features on the left perfectly aligned with the <110> directions? How do you know?

17. [4] The structure below is made in a standard process.

a. What is the process?

b. What are the names and materials of the three different layers that are visible? Draw an arrow identifying each one in the figure.



Haugen, et al. “Integration of Carbon Nanotubes in Microsystems: Local Growth and Electrical Properties of Contacts”, Materials 2013, V6N8.

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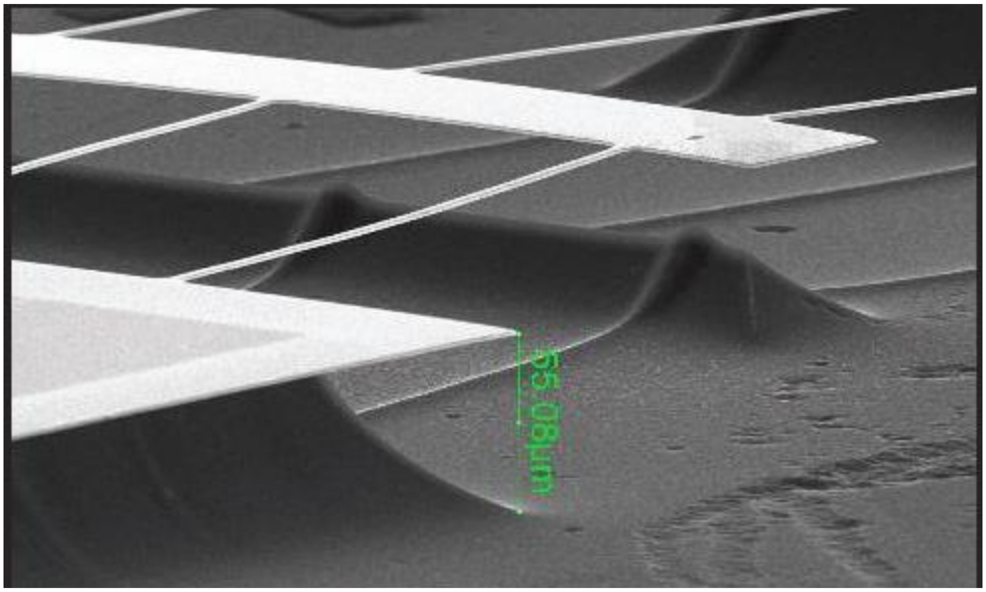
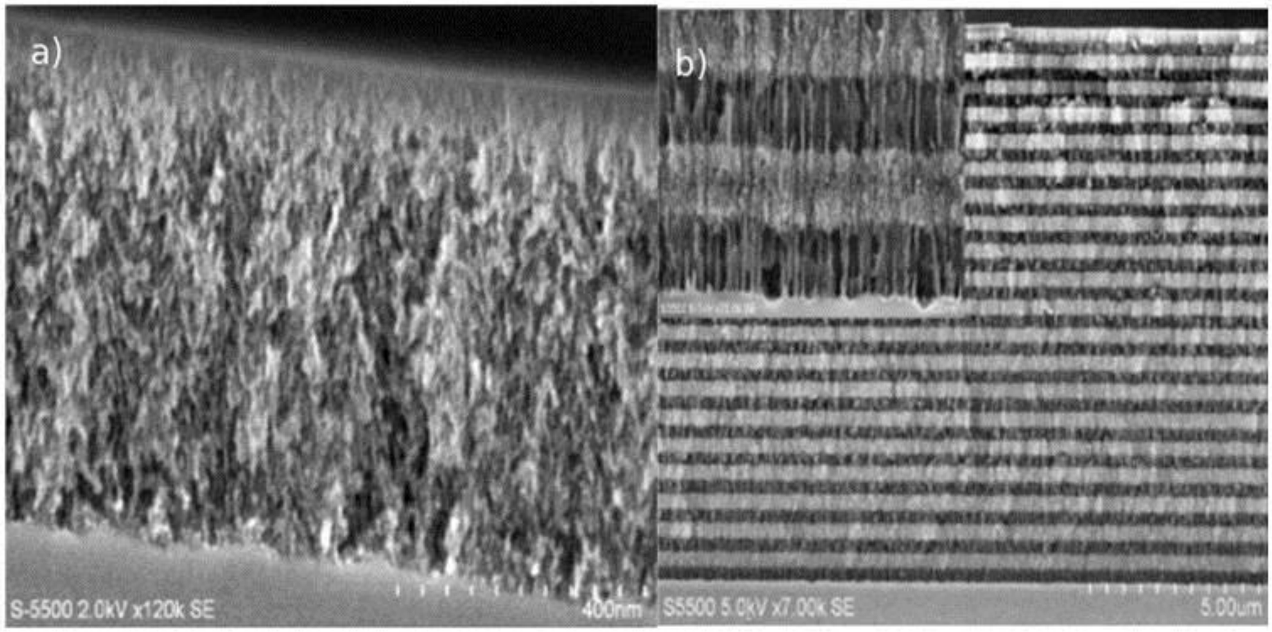
18. [3] The structure below is made from aluminum. The only other material is silicon. Write down a process that could create this structure.



Baracu et al.,"Design and fabrication of a MEMS chevron-type thermal actuator", NANOTEXNOLOGY 2014.

19. [4] The figure below is a cross-section of structures that are all silicon.
    a. What process would produce this?

    b. How would the horizontal stripes be created?



M. B. de la Mora, M. Ocampo, R. Doti, J. E. Lugo and J. Faubert, Chapter 6, "State of the Art in Biosensors - General Aspects"

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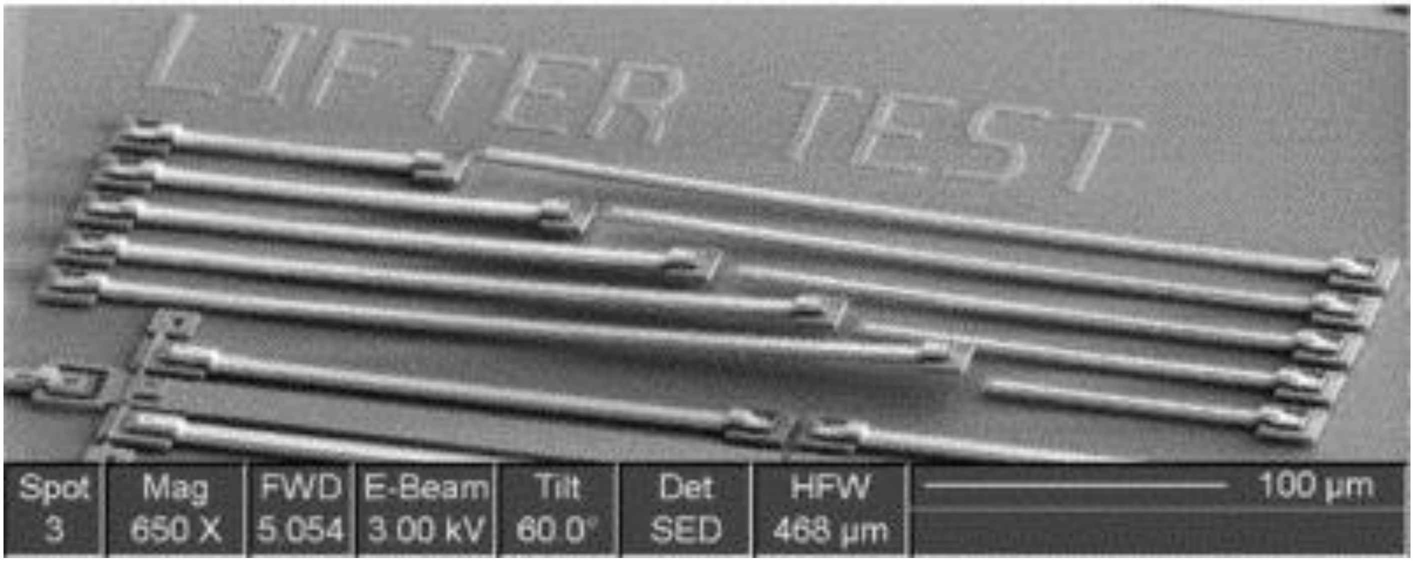
20. [10] The upper left structures in the figure below were made in polyMUMPs, and consist of METAL on POLY2, with POLY1 squares on both ends, one anchored, one free.
    a. Is the stress in the metal tensile or compressive? Why?

    b. Sketch the layout of the shortest structure (upper left), and draw a cross-section (before HF release) beneath your layout.



Johnstone et al., “Non-uniform residual stresses for parallel assembly of out-of-plane […]”, JMM 2006.

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Scratch paper. If you want this work to be graded, make sure that results are boxed and clearly labeled!!!