UNIVERSITY OF CALIFORNIA, BERKELEY

DEPARTMENT OF ELECTRICAL ENGINEERING AND COMPUTER SCIENCES

EECS 130

Fall 2009

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Midterm I

Name: _____

Closed book. One sheet of notes is allowed.

There are 8 pages of this exam including this page.

Problem 1	30
Problem 2	40
Problem 3	30
Total	100

Physical Constants

Electronic charge	q	1.602 x 10 ⁻¹⁹ C
Permittivity of vacuum	ε ₀	$8.845 \text{ x } 10^{-14} \text{ F} \cdot \text{cm}^{-1}$
Relative permittivity of silicon	$\epsilon_{s'} \epsilon_0$	11.8
Relative permittivity of SiO ₂	$\epsilon_{\rm ox/} \epsilon_0$	3.9
Boltzmann's constant	k	$8.617 \ge 10^{-5} \text{eV} \cdot \text{K}^{-1}$ or $1.38 \ge 10^{-5} \text{eV} \cdot \text{K}^{-1}$
		$10^{-23} \text{J} \cdot \text{K}^{-1}$
Thermal voltage at $T = 300$ K	kT/q	0.026V
Effective density of states (Si)	N _c	$2.8 \times 10^{19} \text{cm}^{-3}$
Effective density of states (Si)	N _v	$1.04 \text{ x } 10^{19} \text{ cm}^{-3}$
Silicon Band Gap	E _g	1.12eV
Intrinsic carrier concentration of Si at 300K	n _i	$1.5 \ge 10^{10} \text{cm}^{-3}$
GaAs Band Gap	E _g	1.42eV

Problem 1: Compare Silicon with GaAs

a) Without doping, which material has a larger intrinsic conductivity at room temperature

Conductivity $\sigma = q.n_i.(\mu_n + \mu_p)$ \Rightarrow For GaAs, E_g is lower => n_i is orders of magnitude lower. (2 Pts) (mobility for GaAs is only ~ 5 times higher) So GaAs has lower conducitivity. (1 Pts)

b) Based on band gap, estimate n_i^2 order of magnitude for GaAs.

$$n_i^2 = N_c \cdot N_v \cdot \exp(\frac{-E_g}{kT}) \Longrightarrow \frac{n_i^2 (GaAs)}{n_i^2 (Si)} = \exp(\frac{E_{g,Si} - E_{g,GaAs}}{kT}) \approx 10^{-5} \text{ (2 Pts)}$$
$$\Longrightarrow n_i^2 (GaAs) \approx 10^{15} \, cm^{-3} \, \text{(2 Pts)}$$

Assumed N_c and N_v are the same for both the materials. 10^{14} cm⁻³ is accepted too.

c) GaAs has about the same lattice constant as Si. What is the ratio of mass densities (g/cm³) for GaAs to that of Si. (Atomic Weights of {(Si 28.09), (Ga 69.72), (As 74.92)}

In a unit lattice cell ...there are 8 atoms of Si. And 4 of Ga and 4 of As in case of GaAs. Both of them are given to have same lattice contants... =>Volume is same

Ratio of densities,
$$\frac{\rho_{GaAs}}{\rho_{Si}} = \frac{4 \times M_{Ga} + 4 \times M_{As}}{8 \times M_{Si}} = 0.5 \times \frac{69.72 + 74.92}{28.09} = 2.57$$
 (3 Pts)

d) Assume that effective mass (m_n^*) is $0.07m_0$ for GaAs and $0.26m_0$ for Si, re-estimate the n_i^2 value for GaAs.

N_c would change as $N_c \propto (m_n^*)^{\frac{3}{2}}$

$$n_i^2 = N_c \cdot N_v \cdot \exp(\frac{-E_g}{kT}) \Longrightarrow \frac{n_i^2 (GaAs)}{n_i^2 (Si)} = \left(\frac{m_n *_{GaAs}}{m_n *_{Si}}\right)^{\frac{1}{2}} \exp(\frac{E_{g,Si} - E_{g,GaAs}}{kT})$$
(2 Pts)
$$\Longrightarrow n_i^2 (GaAs) \approx 10^{14} \, cm^{-3}$$
(2 Pts)

3

 10^{13} cm⁻³ is also accepted.

e) Assume identical mean free time, using the effective mass in d), given the mobility of electrons in Silicon is $1200 \text{cm}^2/(\text{V}\cdot\text{s})$, what is the electron mobility in GaAs?

$$\mu_n = \frac{q\tau_n}{m_n^*} \Longrightarrow \mu_n(GaAs) = \mu_n(Si) \times \frac{m_n^* s_i}{m_n^* s_{GaAs}}$$
(3 Pts)
$$\mu_n = 4457 cm^2 / (V \cdot s)$$
(1 Pts)

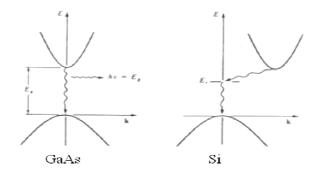
f) What's the ratio of electron diffusion constants for GaAs and Si?

Einstein Relation,
$$\frac{D}{\mu} = \frac{kT}{q} => D$$
 is directly proportional to μ .

$$As \mu = \frac{q\tau}{m^*}, \ \frac{D_{n,GaAs}}{D_{n,Si}} = \frac{\mu_{n,GaAs}}{\mu_{n,Si}} = \frac{m^*_{n,Si}}{m^*_{n,GaAs}}$$
(3 Pts)

$$\frac{D_{n,GaAs}}{D_{n,Si}} = 3.714 \quad (1 \text{ Pts})$$

- g) What is the ratio of the average hole kinetic energy of GaAs and Si at room temperature?
 Average Hole Kinetic Energy = 3kT/2 (Doesn't depend on material) (3 Pts)
 - \Rightarrow *Ratio* =1 (1 Pts)
- h) Given the E-k diagram below



In general do you expect GaAs to have longer or shorter recombination lifetime., assuming both the materials have very few trap states in the band gap. Explain your conclusion concisely.

GaAs will have a shorter recombination lifetime. (2 Pts)

GaAs has a direct bandgap and Si has an indirect bandgap. Recombination in Si has to happen through trap states, while in GaAs direct band to band recombination is possible.

So given we have few trap states in both the materials, the recombination lifetime is perhaps shorter in GaAs due to direct band to band jump. (2 Pts)

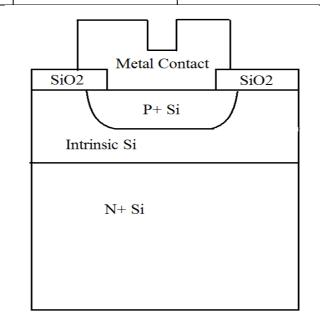
Problem 2 High Power Rectifier P⁺-I-N⁺

Part A Fabrication Process

Given below is a menu consisting of various fabrication processes you are familiar with. Using only these given processes, complete the sequence of steps to fabricate the structure given below. (Hint: All given processes may or may not be required, and the same process maybe used multiple times.)

Processes Available:

Thermal Oxidation	Lithography	Epitaxy	Ion Implantation
Anisotropic Etching	Isotropic Etching	Chemical Vapor Deposition	Nano-Imprint
Sputtering	Chemical Mechanical Polishing	Thermal Annealing	



Process Flow:

- 1. Epitaxy (Grow Intrinsic Silicon above N+ Silicon Substrate)
- 2. Thermal Oxidation (Form SiO₂ thin film)

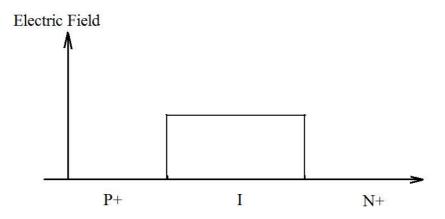
- 3. Lithography (Coat photoresist, pattern contact area and develop photoresist)
- 4. Anisotropic Etching (Etch SiO_2 which is above P+ Si region)
- 5. Ion Implantation (Form P+ doping region)
- 6. Thermal Annealing (Repair damage and diffusion to achieve right junction depth)
- 7. Chemical Vapor Deposition or Sputter (Form Metal Contact)
- 8. Lithography (Pattern trench area of metal)
- 9. Anisotropic Etching (Form the trench of metal)

(2 Pts each step). You may split the formation of metal trenches in two separate steps.

Part B (doable without solving Part A)

The structure in Part A is a high voltage and high current rectifier with a 500V breakdown voltage. Assume that the critical electric field is $2x10^5$ V/cm.

a) Draw the electric field profile through the P⁺-I-N⁺ junction under reverse bias condition.



E-Field has to be constant in intrinsic region (**3** points)...refer HW Assignment and depletion regions in P^+ and N^+ regions are negligible (**2** points). (A threshold for the steepness of edges was set and grading was based on that => steep edges were accepted....)

b) What is the minimum thickness of intrinsic region to achieve the breakdown voltage?

From the plot above all the E-Field is in the intrinsic region => All the voltage drops in this region.

Thickness, t_{intrinsic} = Breakdown Voltage / Critical Field (2 points)

 $=500 V / (2x10^{5} V/cm) = 2.5 x10^{-3} cm (2 points)$

c) What is the advantage of doping substrate $N^{\scriptscriptstyle +}$ rather than lightly doped N-type substrate?

Resistance (2 points)... More doping => lower resistance of the Quasi-Neutral region. Remember the lower part of the substrate is the second contact to this diode. This resistance tends to lower your current for a given voltage. (2 points for reason)

Capacitance increases (as W_{dep} decreases), so this actually tends to make things slower. So this is not accepted as an answer.

d) What is the possible advantage of starting with N⁺ substrate rather than P⁺ substrate in the fabrication process in Part A?

Lower Resistance again (**2** *points*) ... *mobility of holes* < *mobility of electrons. So the P*+ *substrate would have a lower conductivity.* (**2** *points for correct reason*)

Question was unclear.... Everyone was awarded full marks 😊

e) Calculate the capacitance of the diode in F/cm^2 .

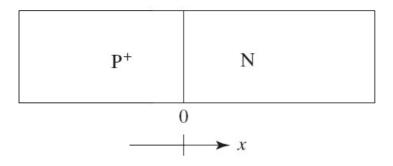
Capacitance, $C = \varepsilon_{0} \cdot \varepsilon_{Si} / W_{dep}$ (2 points)

But $W_{dep} = t_{intrinsic} (2 points)$

 \Rightarrow C = 8.845 x 10⁻¹⁴ F·cm⁻¹ x11.8/2.5 x10⁻³ cm=4.174x10⁻¹⁰ F·cm⁻²(1 point)

Problem 3 Forward-biased diode

Consider a one-sided P⁺N diode as shown in the figure (uniform doping)



a. Draw the electron energy band diagram throughout the diode at V = 0V. Include the Fermi Level(s) in the figure.

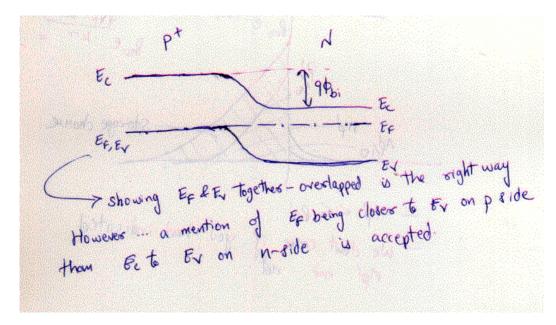


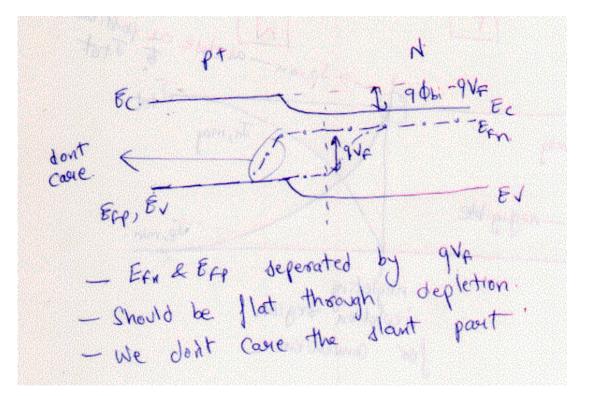
Figure (crude)... (2 points)

Labels important ..., E_C , E_V and E_F . (2 points)

Also relative position of E_F to E_C and E_V on either side important (1 point)

b. How would the band diagram look for a forward bias V_F applied across the diode. Include the quasi-Fermi level(s) through the depletion region too.

Figure (crude)... (2 points) Labels important E_C , E_V , E_{Fn} and E_{Fp} . (2 points) qV_F also needs to be indicated. (1 point)



c. Sketch the excess minority carrier content versus 'x' for two forward bias conditions, one at some value V_F and another at V_F - 60 mV. Show the critical ratio(s).

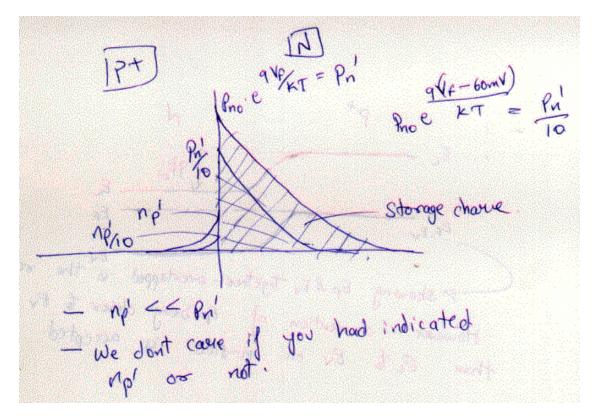


Figure (crude) ... (1 point) Need to show n_p ' << p_n ' (2 points) Labels and Ratio p_n ' and p_n '/10 ... (2 points)

d. Label the stored charge at V_F and V_F -60 mV in the above question. Calculate the ratio of the stored charges at the two biases.

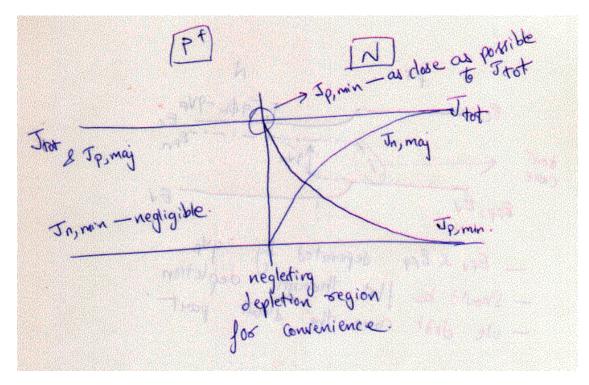
Label ... (2 points)

Ratio ... (3 points) ... Need to show steps ... Else (1.5 points)

For long base diode ... $Q = I.\tau_p$

$$\frac{Q(V_F)}{Q(V_F - 60mV)} = \frac{\tau_p . I_0 . e^{\frac{qV_F}{kT}}}{\tau_p . I_0 . e^{\frac{q(V_F - 60mV)}{kT}}} = e^{\frac{q.60mV}{kT}} \approx 10,$$

e. Draw the minority, majority and total current densities versus '*x*'. Label the components clearly.



Label ... J_{totab} J_{maj} and J_{min} at least for lowly doped side; (2 points)

 J_{tot} and J_{maj} for highly doped side. (2 points)

For indicating J_{min} on highly doped side as small / not present and J_{min} at edge of depletion for lightly doped side to be as close to J_{total} . (**1** point)

f. For a forward bias of 0.7 V, find the ratio of stored charge (up to the order of magnitude) at T = 300 K and T = 500 K.

Not particular about the answer but dependencies.

Temperature Dependence in $D_p(2 \text{ points})$, $n_i^2 (T^3 \text{ and } exp(-E_g/kT))(1 \text{ point} + 1 \text{ point})$ and exp(qV/kT) (1 point)

But if assumptions were made and mentioned for D_p and $n_i^2(T^3)$ parts... it is accepted.

$$\begin{split} Q &= I \times \tau \\ I \approx I_0 \cdot (e^{\frac{qV}{kT}} - 1); I_0 = qA \frac{n_i^2}{N_D} \sqrt{\frac{D_p}{\tau_p}}; D_p = \mu_p \cdot \frac{kT}{q} \\ n_i^2 &= N_c \cdot N_v \cdot e^{\frac{-E_g}{kT}} \\ \frac{Q(300K)}{Q(500K)} &= \frac{I_0(300K)(e^{\frac{q \cdot 0.7}{k \cdot 300K}} - 1)}{I_0(500K)(e^{\frac{q \cdot 0.7}{k \cdot 500K}} - 1)} = \frac{n_i^2(300K)}{n_i^2(500K)} \cdot \sqrt{\frac{300}{500}} \cdot \frac{(e^{\frac{q \cdot 0.7}{k \cdot 300K}} - 1)}{(e^{\frac{q \cdot 0.7}{k \cdot 500K}} - 1)} \\ &= \left(\frac{300}{500}\right)^3 \exp\left(\frac{-q \times 1.12}{k} \left(\frac{1}{300} - \frac{1}{500}\right)\right) \times \sqrt{\frac{300}{500}} \times \frac{(e^{\frac{q \cdot 0.7}{k \cdot 300K}} - 1)}{(e^{\frac{q \cdot 0.7}{k \cdot 500K}} - 1)} = 1.14 \times 10^{-4} \end{split}$$

Assuming μ_p is roughly constant